



CAN Matrix
I4 12.100.5
500kbps Powertrain CAN

| 0x740 | Channel | Start Bit | Length | Type | Range | Unit |
|--------------------------------------|-----------------|-----------|--------|--------------|---------|---------|
| I4_Status Periodic 20ms DLC: 8 | Firmware ID | 0 | 8 | unsigned int | 0-255 | # |
| | Firmware Major | 8 | 8 | unsigned int | 0-255 | # |
| | Firmware Minor | 16 | 8 | unsigned int | 0-255 | # |
| | Error | 24 | 1 | bit | 0-1 | boolean |
| | CAN Timeout | 32 | 1 | bit | 0-1 | boolean |
| | Slave Heartbeat | 40 | 8 | unsigned int | 0-255 | # |
| | Battery Voltage | 48 | 16 | unsigned int | 0-65535 | mV |

| 0x741 | Channel | Start Bit | Length | Type | Range | Unit |
|-----------------------------------------|----------|-----------|--------|--------------|---------|------|
| I4_Analog Tx Periodic 20ms DLC: 8 | Analog 1 | 0 | 16 | unsigned int | 0-65535 | mV |
| | Analog 2 | 16 | 16 | unsigned int | 0-65535 | mV |
| | Analog 3 | 32 | 16 | unsigned int | 0-65535 | mV |
| | Analog 4 | 48 | 16 | unsigned int | 0-65535 | mV |

| 0x742 | Channel | Start Bit | Length | Type | Range | Unit |
|------------------------------------------|---------------------|-----------|--------|--------------|---------|---------|
| I4_Digital Tx Periodic 20ms DLC: 8 | Digital 1 Frequency | 0 | 15 | unsigned int | 0-32767 | Hz |
| | Digital 1 State | 15 | 1 | bit | 0-1 | boolean |
| | Digital 2 Frequency | 16 | 15 | unsigned int | 0-32767 | Hz |
| | Digital 2 State | 31 | 1 | bit | 0-1 | boolean |
| | Digital 3 Frequency | 32 | 15 | unsigned int | 0-32767 | Hz |
| | Digital 3 State | 47 | 1 | bit | 0-1 | boolean |
| | Digital 4 Frequency | 48 | 15 | unsigned int | 0-32767 | Hz |
| Digital 4 State | 63 | 1 | bit | 0-1 | boolean | |

| 0x743 | Channel | Start Bit | Length | Type | Range | Unit |
|----------------------------------------|------------------------|-----------|--------|------|-------|---------|
| I4_State Tx Periodic 20ms DLC: 8 | Digital 1 Pullup State | 32 | 1 | bit | 0-1 | boolean |
| | Digital 2 Pullup State | 40 | 1 | bit | 0-1 | boolean |
| | Digital 3 Pullup State | 48 | 1 | bit | 0-1 | boolean |
| | Digital 4 Pullup State | 56 | 1 | bit | 0-1 | boolean |

| 0x744 | Channel | Start Bit | Length | Type | Range | Unit |
|--------------------------------------|--------------------|-----------|--------|------|-------|---------|
| I4_Command Rx on change DLC: 8 | Reset | 40 | 1 | bit | 0-1 | boolean |
| | Master Enable | 48 | 1 | bit | 0-1 | boolean |
| | Output Test Enable | 56 | 1 | bit | 0-1 | boolean |

| 0x745 | Channel | Start Bit | Length | Type | Range | Unit |
|-------------------------------------|------------------|-----------|--------|--------------|-------|---------|
| I4_Config Rx on change DLC: 8 | Analog 1 Filter | 0 | 8 | unsigned int | 0-100 | # |
| | Analog 2 Filter | 8 | 8 | unsigned int | 0-100 | # |
| | Analog 3 Filter | 16 | 8 | unsigned int | 0-100 | # |
| | Analog 4 Filter | 24 | 8 | unsigned int | 0-100 | # |
| | Digital 1 Pullup | 32 | 1 | bit | 0-1 | boolean |
| | Digital 2 Pullup | 40 | 1 | bit | 0-1 | boolean |
| | Digital 3 Pullup | 48 | 1 | bit | 0-1 | boolean |
| | Digital 4 Pullup | 56 | 1 | bit | 0-1 | boolean |